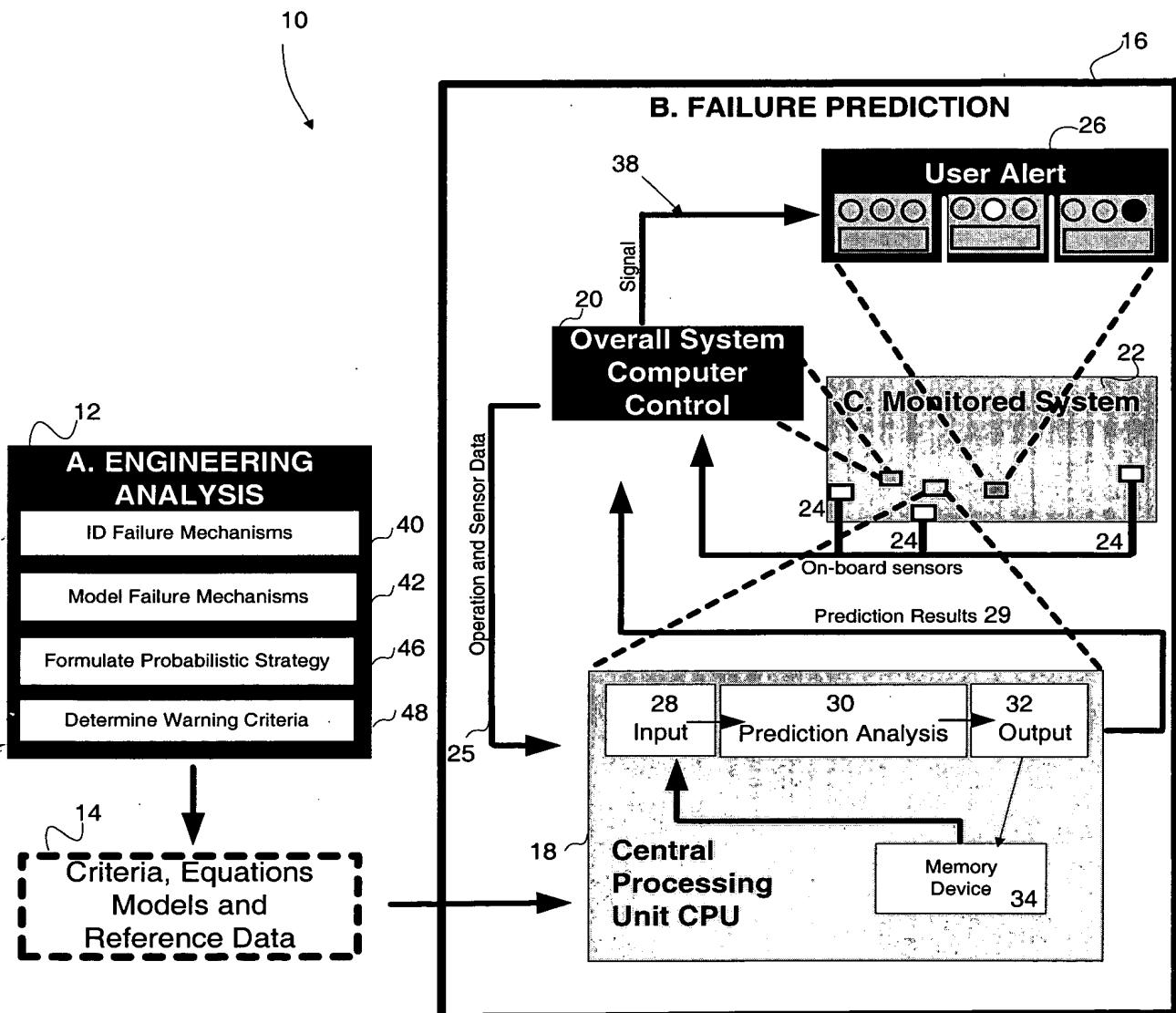
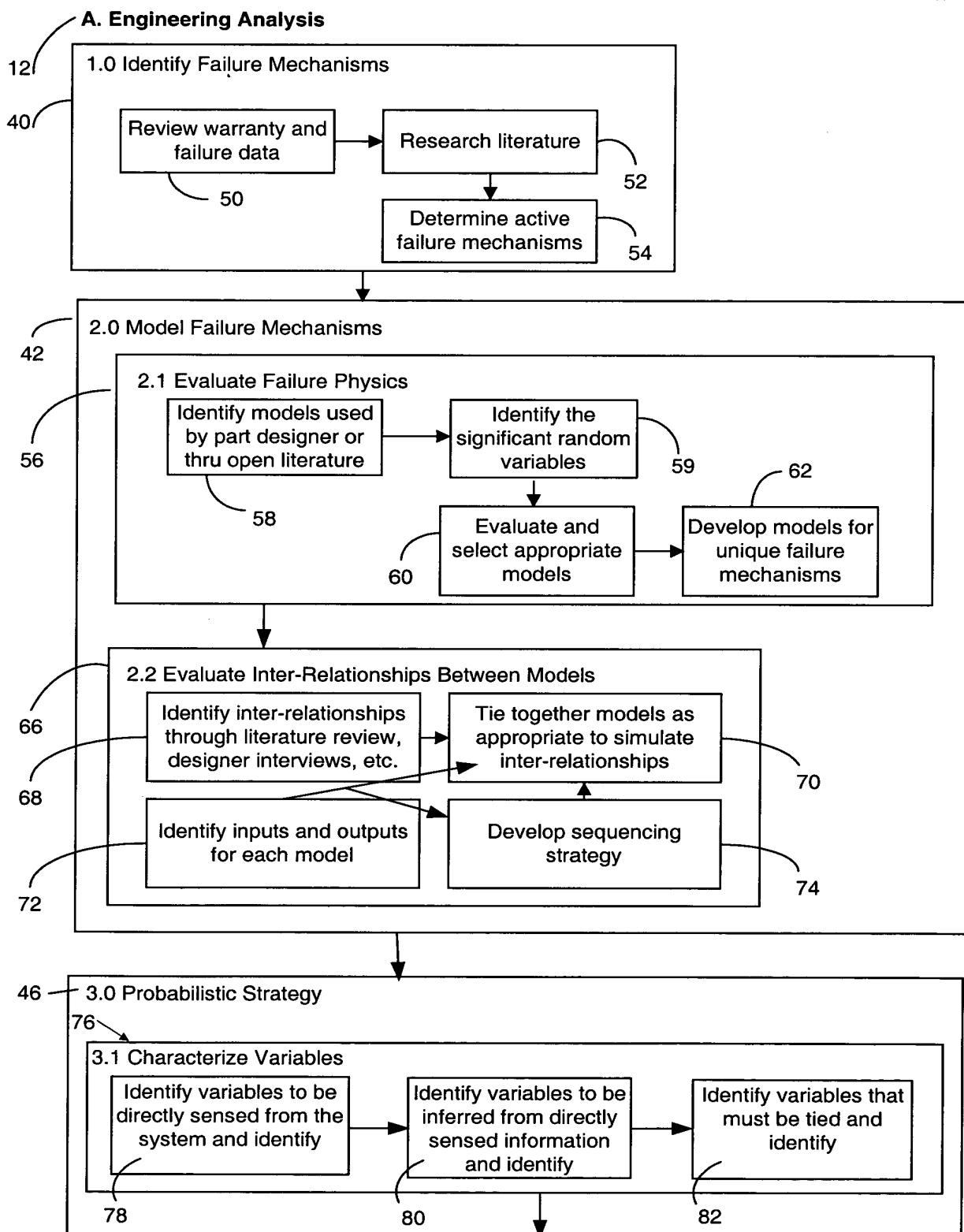
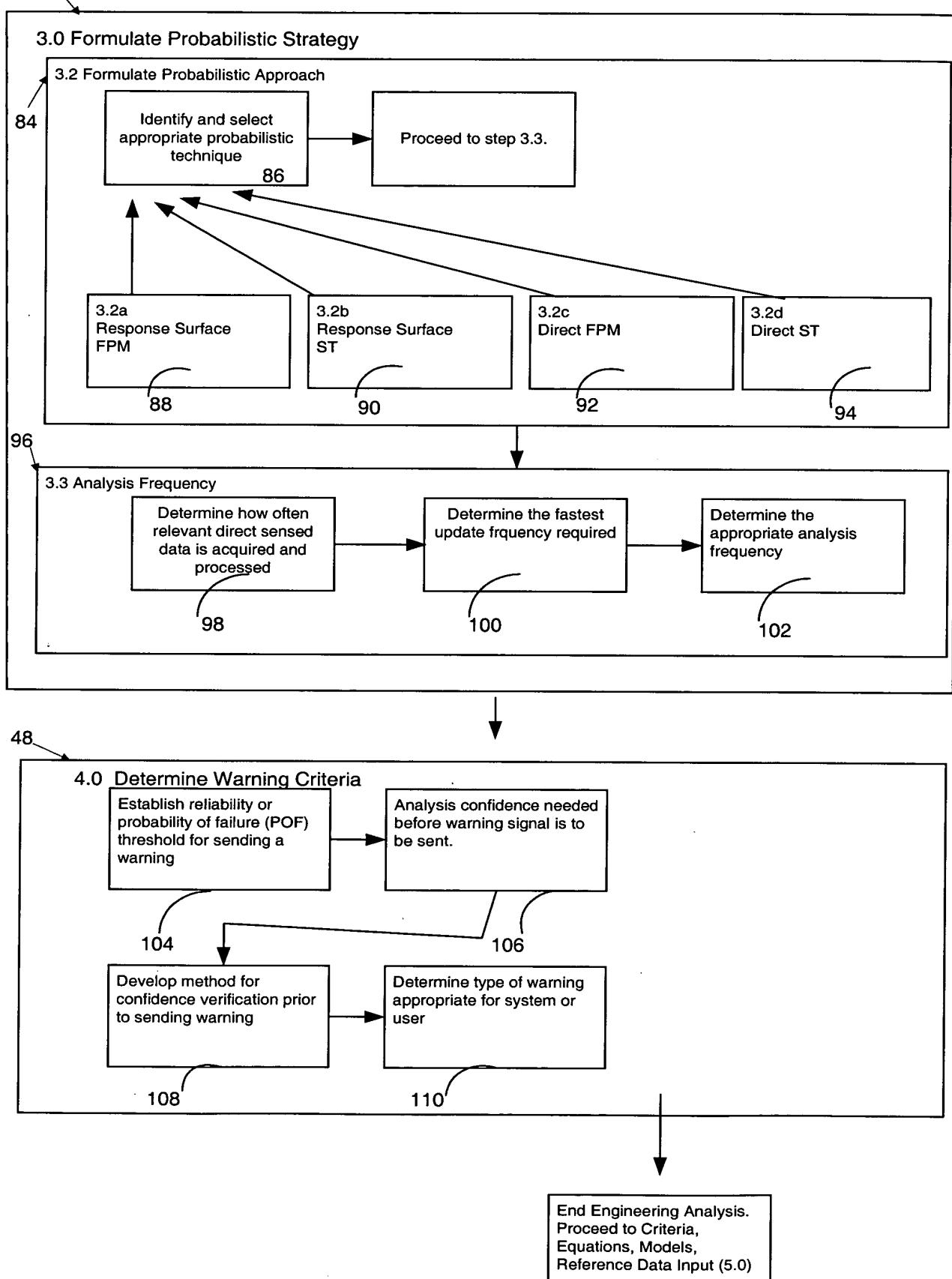


**FIGURE 1**  
**ON-BOARD PROGNOSTIC INSTRUMENT ENGINEER  
(OPIE)**

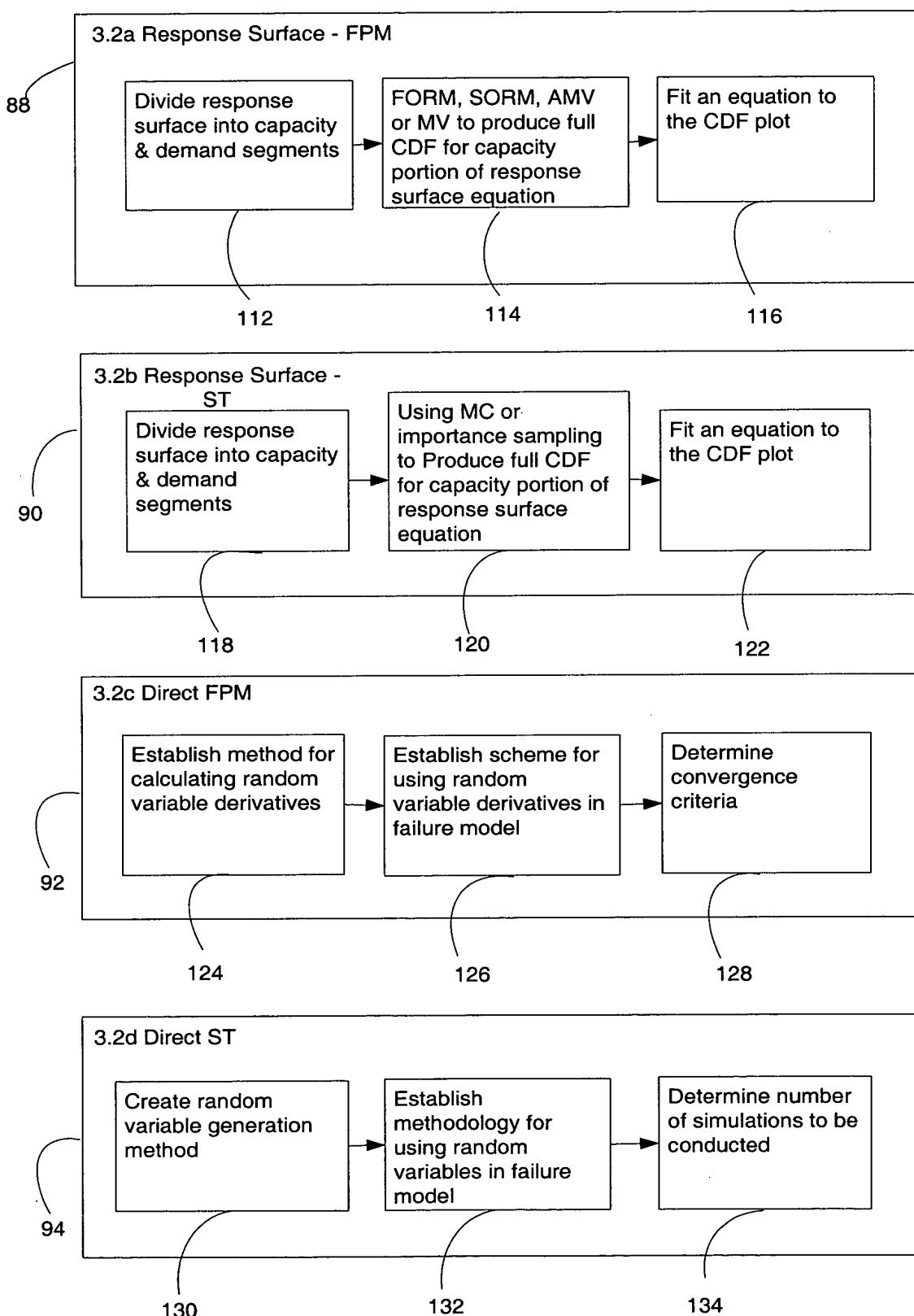


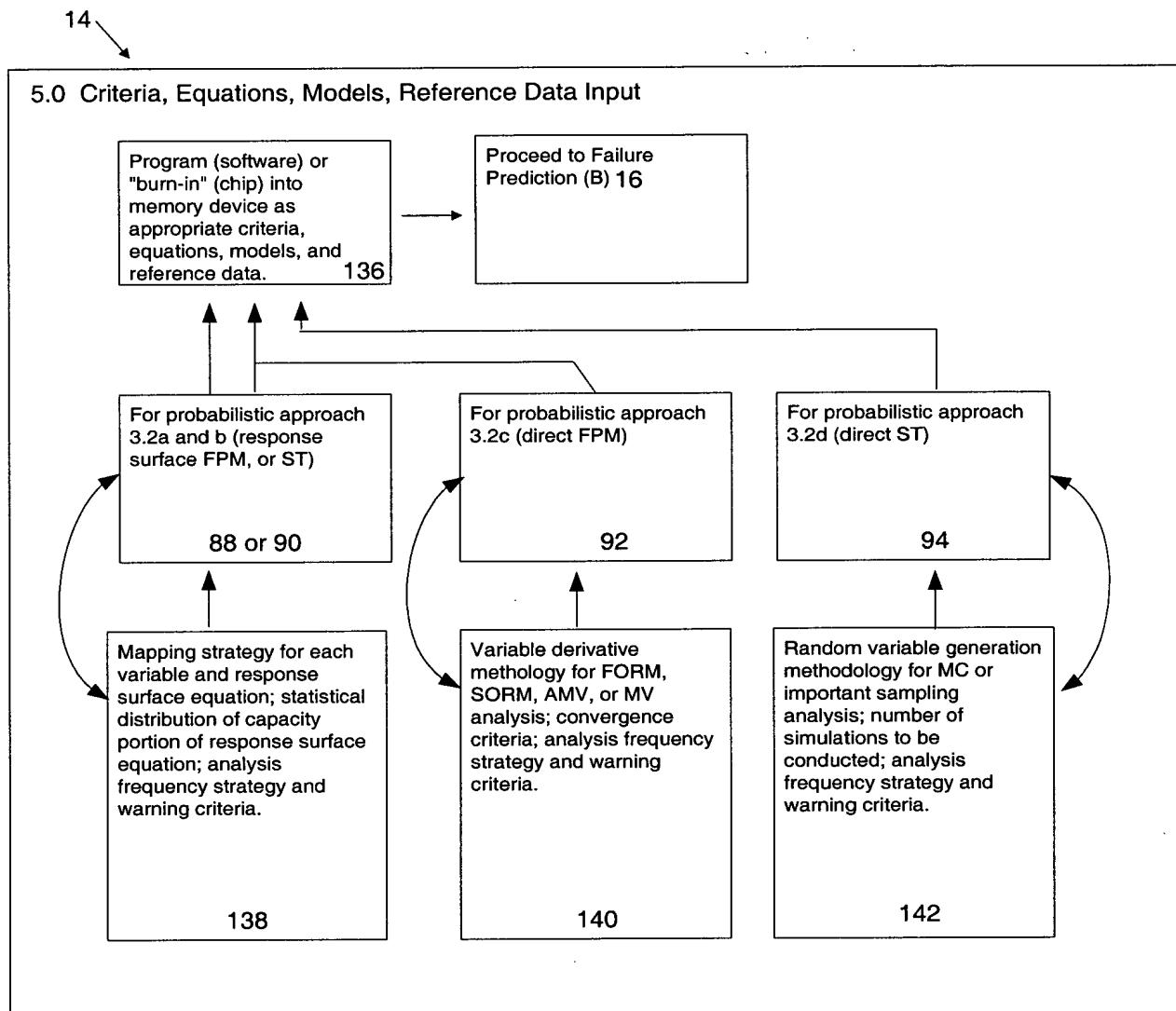
**Figure 2A**



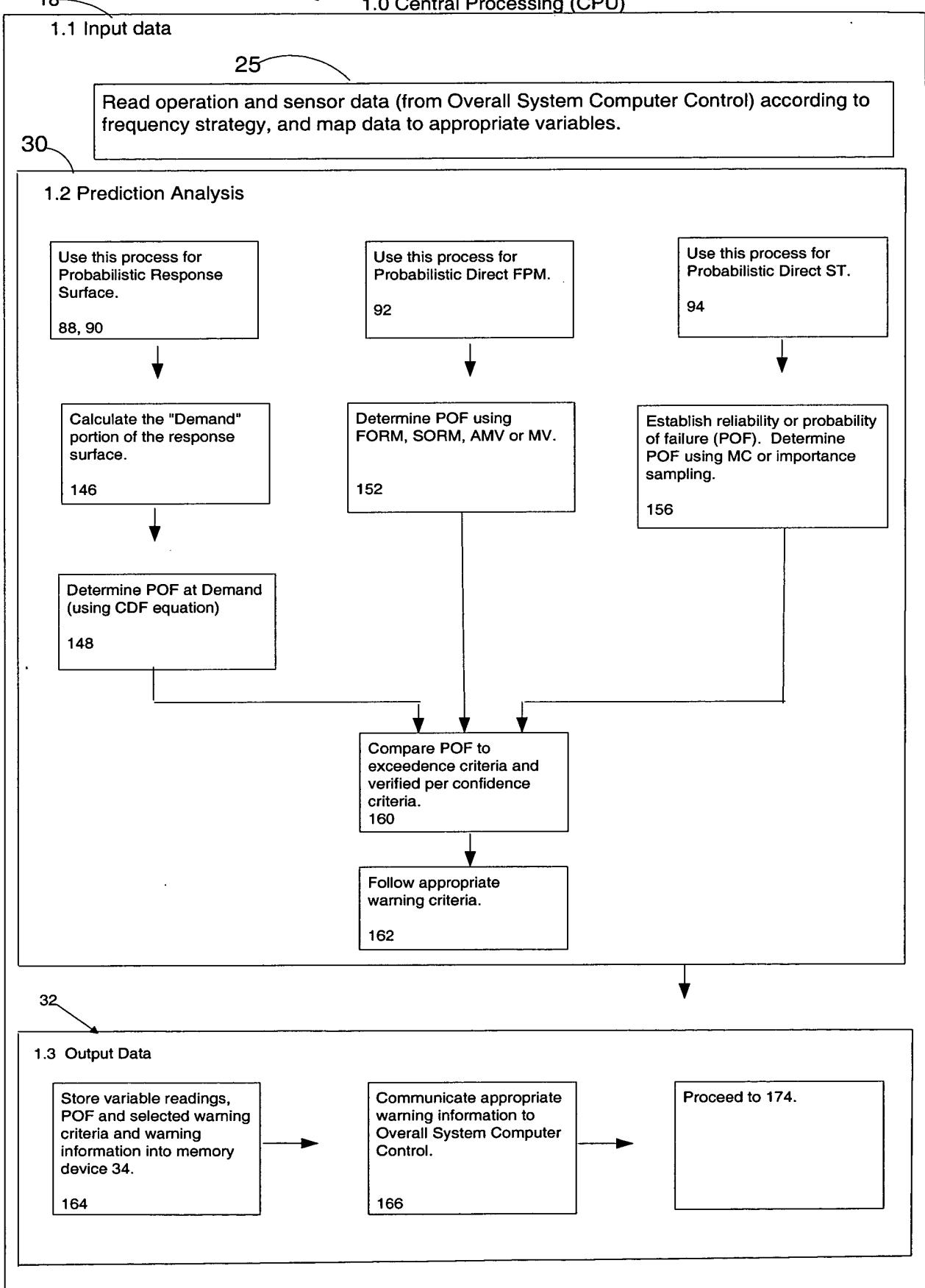
46  
Cont'd**Figure 2B**

**FIGURE 2C**



**Figure 2D**

**Figure 3A**  
1.0 Central Processing (CPU)



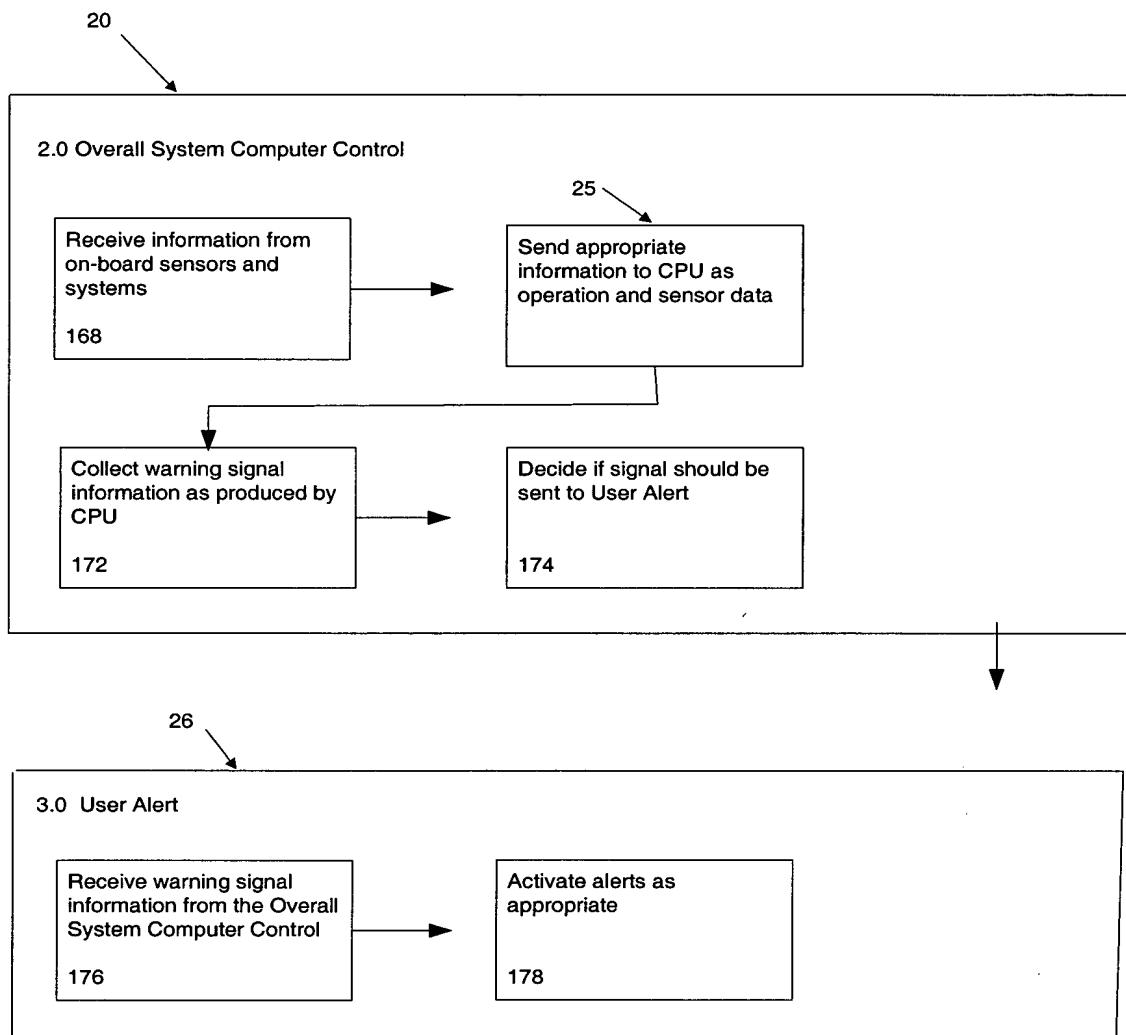
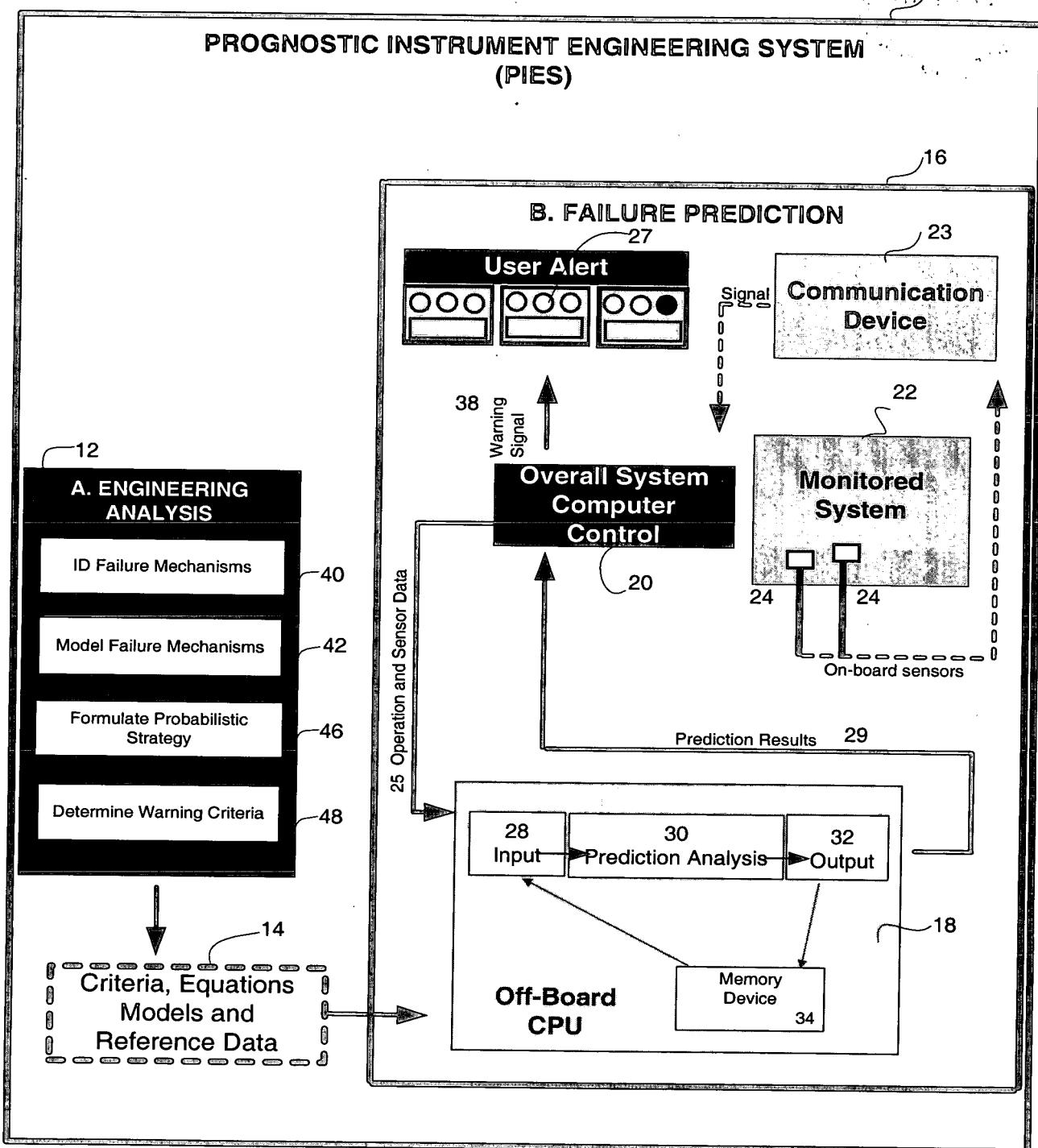
**Figure 3B**

Figure 4



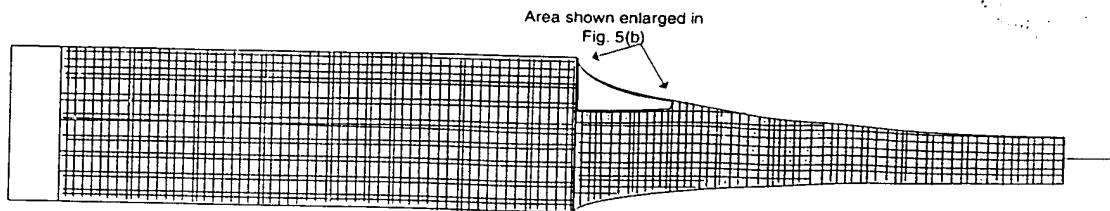


Figure 5(a)

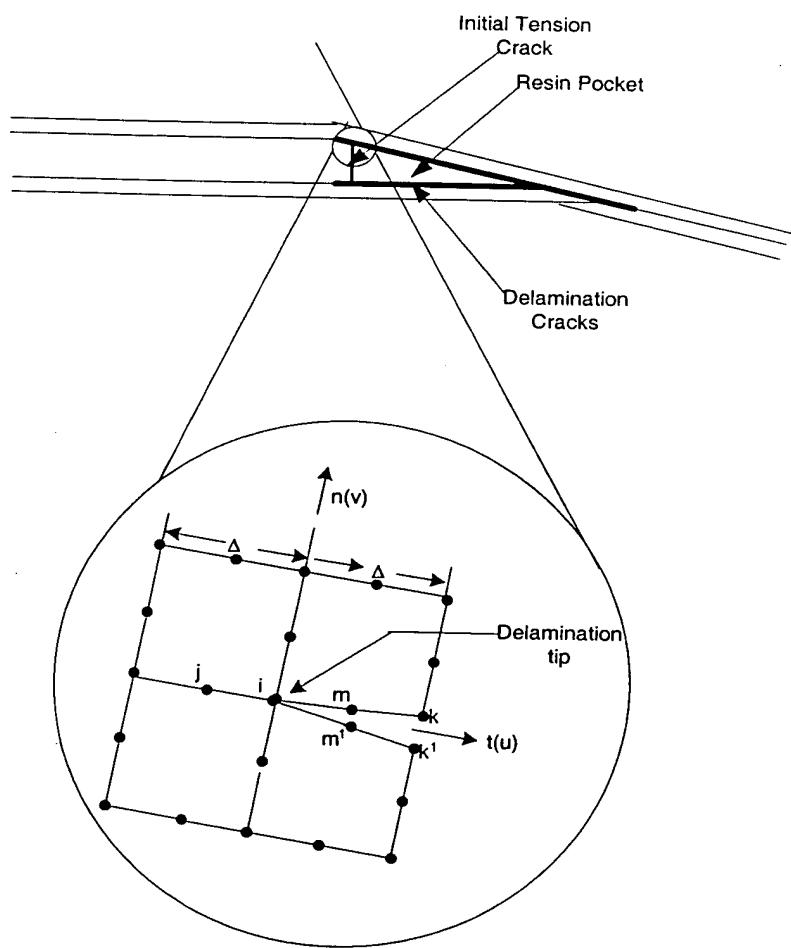


Figure 5(b)

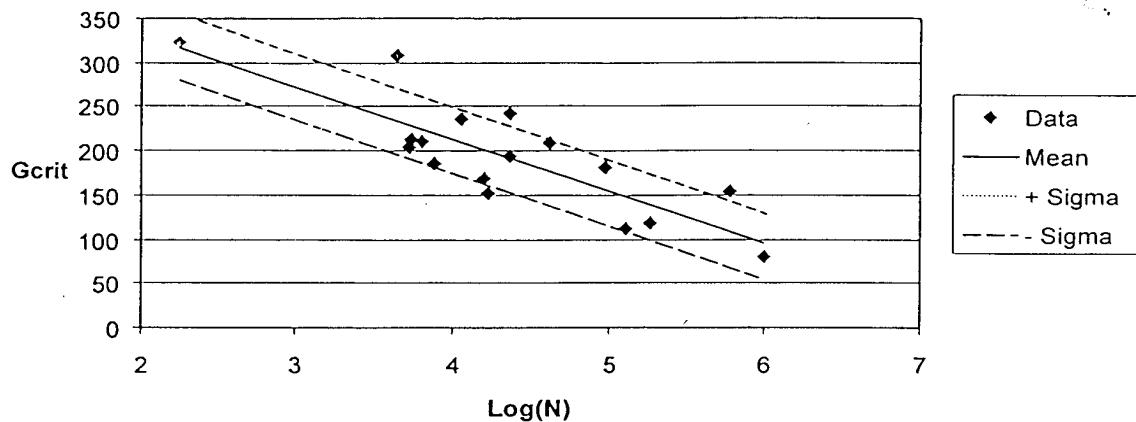


Figure 5(c)

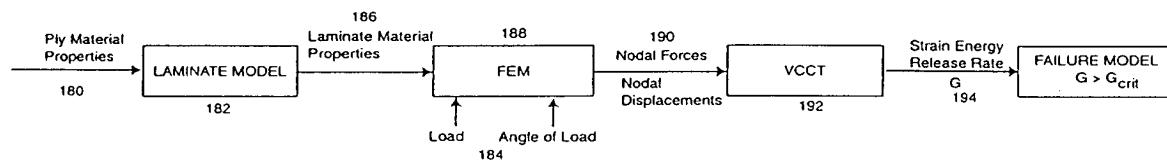


Figure 5(d)

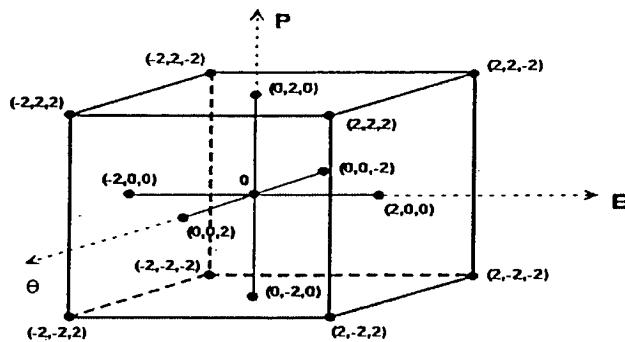
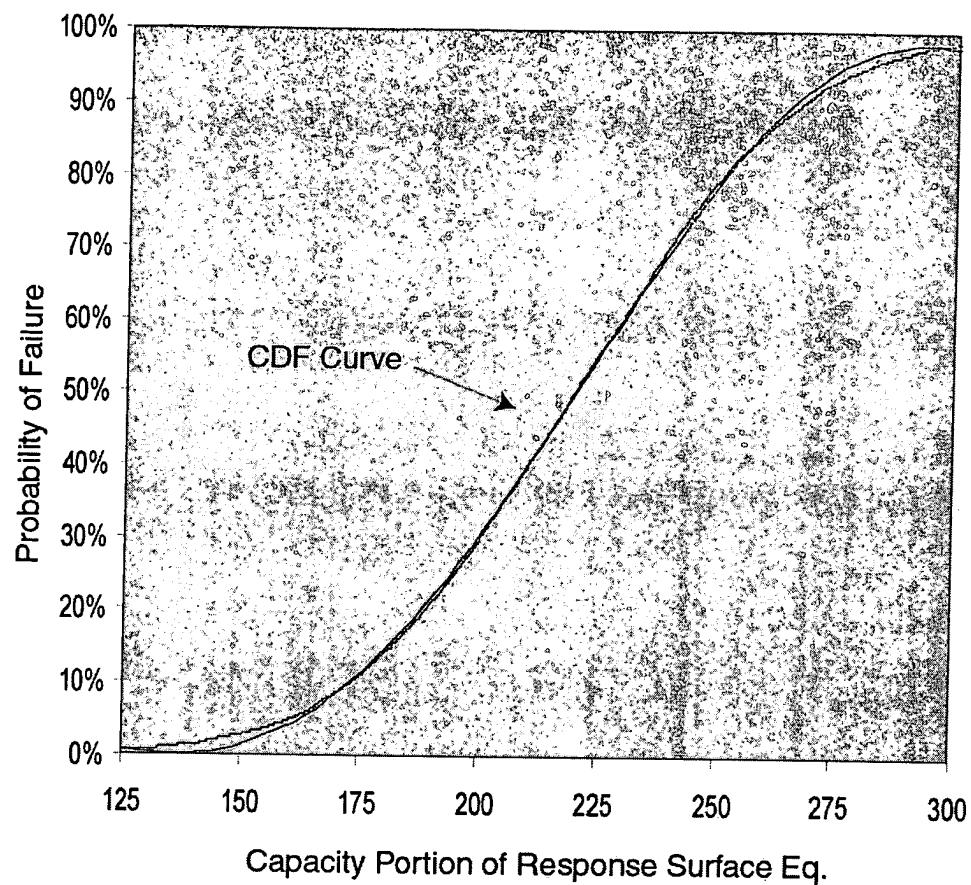


Figure 5(e)



**Figure 5(f)**